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PLUS Search Results for S/N 09822190, Searched December 02, 2004

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5581566
5793770
5468992
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5845085
6023202
4402067
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5309394
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6424401
6593826
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6170075
4446524
4612546
5315594
5365272
5442810
5506956
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5555274
5577196
5583863
5592554
5606553
5619491
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Most Frequently Occurring Classifications of Patents Returned
From A Search of 09822190 on December 02, 2004

Original Classifications

3 257/676
3 712/33
2 607/57

Cross-Reference Classifications

6 257/E23.175
6 257/E25.011
5 257/635
5 257/638
5 257/678
5 257/E23.079
4 257/E23.169
3 361/719
3 361/792
2 257/211
2 257/700
2 327/525
2 331/177V
2 331/36C
2 365/189.01
2 370/465
2 380/240
2 709/250
2 712/38
2 712/39
2 714/821

Combined Classifications

6 257/E23.175
6 257/E25.011
5 257/635
5 257/638
5 257/678
5 257/E23.079
4 257/E23.169
3 257/676
3 361/719
3 361/792
3 370/465
3 712/33
2 257/211
2 257/700
2 327/525

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2 331/177V
2 331/36C
2 365/189.01
2 365/225.7
2 370/395.7
2 370/401
2 370/420
2 370/497
2 370/522
2 375/232
2 380/240
2 380/30
2 607/57
2 709/250
2 712/38
2 712/39
2 714/821

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Titles of Most Frequently Occurring Classifications of Patents Returned

From A Search of 09822190 on December 02, 2004

6 257/E23.175 (0 OR, 6 XR)
 Class 257 : ACTIVE SOLID-STATE DEVICES
 257/E23.139 ...Liquid at normal operating temperature of device (EPO)
 257/E23.141 .Arrangements for conducting electric current within device in operation from one component to another, interconnections, e.g., wires, lead frames (EPO)
 257/E23.169 ..Interconnection structure between plurality of semiconductor chips being formed on or in insulating substrates (EPO)
 257/E23.175 ...Geometry or layout of interconnection structure (EPO)

6 257/E25.011 (0 OR, 6 XR)
 Class 257 : ACTIVE SOLID-STATE DEVICES
 257/E25.001 ASSEMBLIES CONSISTING OF PLURALITY OF INDIVIDUAL SEMICONDUCTOR OR OTHER SOLID-STATE DEVICES (EPO)
 257/E25.002 .All devices being of same type, e.g., assemblies of rectifier diodes (EPO)
 257/E25.003 ..Devices not having separate containers (EPO)
 257/E25.01 ...Device consisting of plurality of semiconductor or other solid state devices or components formed in or on common substrate, e.g., in integrated circuit device (EPO)
 257/E25.011Devices being arranged next and on each other, i.e., mixed assemblies (EPO)

5 257/635 (0 OR, 5 XR)
 Class 257 : ACTIVE SOLID-STATE DEVICES
 257/629 WITH MEANS TO CONTROL SURFACE EFFECTS
 257/632 .Insulating coating
 257/635 ..Multiple layers

5 257/638 (0 OR, 5 XR)

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Class 257 : ACTIVE SOLID-STATE DEVICES
 257/629 WITH MEANS TO CONTROL SURFACE EFFECTS
 257/632 .Insulating coating
 257/635 ..Multiple layers
 257/638 ...With discontinuous or varying thickness
 layer (e.g., layer covers only selected por
 tions of
 semiconductor)

5 257/678 (0 OR, 5 XR)
 Class 257 : ACTIVE SOLID-STATE DEVICES
 257/678 HOUSING OR PACKAGE

5 257/E23.079 (0 OR, 5 XR)
 Class 257 : ACTIVE SOLID-STATE DEVICES
 257/E23.001 PACKAGING, INTERCONNECTS, AND MARKINGS FOR
 SEMICONDUCTOR OR OTHER SOLID-STATE DEVICE

S (EPO)
 257/E23.01 .Arrangements for conducting electric current
 to or from solid-state body in operation,
 e.g., leads,
 terminal arrangements (EPO)
 257/E23.079 ..For integrated circuit devices, e.g., power
 bus, number of leads (EPO)

4 257/E23.169 (0 OR, 4 XR)
 Class 257 : ACTIVE SOLID-STATE DEVICES
 257/E23.139 ...Liquid at normal operating temperature of
 device (EPO)
 257/E23.141 .Arrangements for conducting electric current
 within device in operation from one compon
 ent to another,
 interconnections, e.g., wires, lead frames
 (EPO)
 257/E23.169 ..Interconnection structure between plurality
 of semiconductor chips being formed on or i
 n insulating
 substrates (EPO)

3 257/676 (3 OR, 0 XR)
 Class 257 : ACTIVE SOLID-STATE DEVICES
 257/666 LEAD FRAME
 257/676 .With structure for mounting semiconductor chi
 p
 to lead frame (e.g., configuration of die b
 onding flag,
 absence of a die bonding flag, recess for L
 ED)

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3 361/719 (0 OR, 3 XR)
 Class 361 : ELECTRICITY: ELECTRICAL SYSTEMS AND DEVICES
 361/600 HOUSING OR MOUNTING ASSEMBLIES WITH DIVERSE
 ELECTRICAL COMPONENTS
 361/679 .For electronic systems and devices
 361/688 ..With cooling means
 361/704 ...Thermal conduction
 361/717For active solid state devices
 361/718For integrated circuit
 361/719Circuit board mounted

3 361/792 (0 OR, 3 XR)
 Class 361 : ELECTRICITY: ELECTRICAL SYSTEMS AND DEVICES
 361/600 HOUSING OR MOUNTING ASSEMBLIES WITH DIVERSE
 ELECTRICAL COMPONENTS
 361/679 .For electronic systems and devices
 361/748 ..Printed circuit board
 361/784 ...Plural
 361/792Plural contiguous boards

3 370/465 (1 OR, 2 XR)
 Class 370 : MULTIPLEX COMMUNICATIONS
 370/464 COMMUNICATION TECHNIQUES FOR INFORMATION
 CARRIED IN PLURAL CHANNELS
 370/465 .Adaptive

3 712/33 (3 OR, 0 XR)
 Class 712 : ELECTRICAL COMPUTERS AND DIGITAL PROCESSING
 SYSTEMS: PROCESSING ARCHITECTURES AND INS

TRUCTION

PROCESSING
 712/1 PROCESSING ARCHITECTURE
 712/32 .Microprocessor or multichip or multimodule
 processor having sequential program contro
 1 712/33 ..Having multiple internal buses

2 257/211 (0 OR, 2 XR)
 Class 257 : ACTIVE SOLID-STATE DEVICES
 257/202 GATE ARRAYS
 257/208 .With particular signal path connections
 257/211 ..Multi-level metallization

2 257/700 (0 OR, 2 XR)
 Class 257 : ACTIVE SOLID-STATE DEVICES

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257/688 .With large area flexible electrodes in press
 contact with opposite sides of active sem
 iconductor chip
 and surrounded by an insulating element,
 e.g., ring

257/690 .With contact or lead
 257/700 ..Multiple contact layers separated from each
 other by insulator means and forming part o
 f a package or
 housing (e.g., plural ceramic layer package
)

2 327/525 (0 OR, 2 XR)
 Class 327 : MISCELLANEOUS ACTIVE ELECTRICAL NONLINEAR
 DEVICES, CIRCUITS, AND SYSTEMS
 327/524 SPECIFIC IDENTIFIABLE DEVICE, CIRCUIT, OR
 SYSTEM
 327/525 .Fusible link or intentional destruct circuit

2 331/177V (0 OR, 2 XR)
 Class 331 : OSCILLATORS
 331/177R WITH FREQUENCY ADJUSTING MEANS
 331/177V .With voltage sensitive capacitor

2 331/36C (0 OR, 2 XR)
 Class 331 : OSCILLATORS
 331/1R AUTOMATIC FREQUENCY STABILIZATION USING A PHAS
 E
 OR FREQUENCY SENSING MEANS
 331/34 .Particular frequency control means
 331/36R ..Reactance device (e.g., variable capacitors,
 saturable inductors, reactance tubes, etc.
)
 331/36C ...Capacitor controlled AFC

2 365/189.01 (0 OR, 2 XR)
 Class 365 : STATIC INFORMATION STORAGE AND RETRIEVAL
 365/189.01 READ/WRITE CIRCUIT

2 365/225.7 (1 OR, 1 XR)
 Class 365 : STATIC INFORMATION STORAGE AND RETRIEVAL
 365/189.01 READ/WRITE CIRCUIT
 365/225.7 .Having fuse element

2 370/395.7 (1 OR, 1 XR)
 Class 370 : MULTIPLEX COMMUNICATIONS
 370/351 PATHFINDING OR ROUTING

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370/389 .Switching a message which includes an address header
 370/395.1 ..Message transmitted using fixed length packets (e.g., ATM cells)
 370/395.7 ...Having detail of switch memory reading/writing

2 370/401 (1 OR, 1 XR)

Class 370 : MULTIPLEX COMMUNICATIONS
 370/351 PATHFINDING OR ROUTING
 370/389 .Switching a message which includes an address header
 370/400 ..Having a plurality of nodes performing distributed switching
 370/401 ...Bridge or gateway between networks

2 370/420 (1 OR, 1 XR)

Class 370 : MULTIPLEX COMMUNICATIONS
 370/351 PATHFINDING OR ROUTING
 370/389 .Switching a message which includes an address header
 370/419 ..Input or output circuit, per se (i.e., line interface)
 370/420 ...For connecting plural subscribers to a network (i.e., network termination)

2 370/497 (1 OR, 1 XR)

Class 370 : MULTIPLEX COMMUNICATIONS
 370/473 ..Transmission of a single message having multiple packets
 370/480 .Combining or distributing information via frequency channels
 370/497 ..Using particular filtering technique

2 370/522 (1 OR, 1 XR)

Class 370 : MULTIPLEX COMMUNICATIONS
 370/473 ..Transmission of a single message having multiple packets
 370/498 .Combining or distributing information via time channels
 370/522 ..Signaling (ancillary to main information)

2 375/232 (1 OR, 1 XR)

Class 375 : PULSE OR DIGITAL COMMUNICATIONS
 375/229 EQUALIZERS
 375/230 .Automatic
 375/232 ..Adaptive

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|-----------|---------|--------------|--|
| 2 | 380/240 | (0 OR, 2 XR) | |
| | Class | 380 : | CRYPTOGRAPHY |
| | 380/200 | | VIDEO CRYPTOGRAPHY |
| | 380/210 | | .Video electric signal modification (e.g., scrambling) |
| | 380/240 | | ..Including addressed decoder control signal |
| | | | |
| 2 | 380/30 | (1 OR, 1 XR) | |
| | Class | 380 : | CRYPTOGRAPHY |
| | 380/28 | | PARTICULAR ALGORITHMIC FUNCTION ENCODING |
| | 380/30 | | .Public key |
| | | | |
| 2 | 607/57 | (2 OR, 0 XR) | |
| | Class | 607 : | SURGERY: LIGHT, THERMAL, AND ELECTRICAL APPLICATION |
| | 607/1 | | LIGHT, THERMAL, AND ELECTRICAL APPLICATION |
| | 607/2 | | .Electrical therapeutic systems |
| | 607/55 | | ..Promoting auditory function |
| | 607/56 | | ...Producing aural effects by stimulation |
| | 607/57 | |By partially or wholly implanted device |
| | | | |
| 2 | 709/250 | (0 OR, 2 XR) | |
| | Class | 709 : | ELECTRICAL COMPUTERS AND DIGITAL PROCESSING SYSTEMS: MULTIPLE COMPUTER OR PROCESS COO |
| RDINATING | 709/200 | | MULTICOMPUTER DATA TRANSFERRING |
| | 709/250 | | .Network-to-computer interfacing |
| | | | |
| 2 | 712/38 | (0 OR, 2 XR) | |
| | Class | 712 : | ELECTRICAL COMPUTERS AND DIGITAL PROCESSING SYSTEMS: PROCESSING ARCHITECTURES AND INS |
| TRUCTION | | | PROCESSING |
| | 712/1 | | PROCESSING ARCHITECTURE |
| | 712/32 | | .Microprocessor or multichip or multimodule processor having sequential program contro |
| 1 | 712/38 | | ..Offchip interface |
| | | | |
| 2 | 712/39 | (0 OR, 2 XR) | |
| | Class | 712 : | ELECTRICAL COMPUTERS AND DIGITAL PROCESSING SYSTEMS: PROCESSING ARCHITECTURES AND INS |
| TRUCTION | | | PROCESSING |
| | 712/1 | | PROCESSING ARCHITECTURE |
| | 712/32 | | .Microprocessor or multichip or multimodule processor having sequential program contr |

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712/38 ..Offchip interface
712/39 ...Externally controlled internal mode
switching via pin

2 714/821 (0 OR, 2 XR)

Class 714 : ERROR DETECTION/CORRECTION AND FAULT
DETECTION/RECOVERY

714/699 PULSE OR DATA ERROR HANDLING
714/799 .Error/fault detection technique
714/819 ..Comparison of data
714/820 ...Plural parallel devices of channels
714/821Transmission facility